

FIG. 1

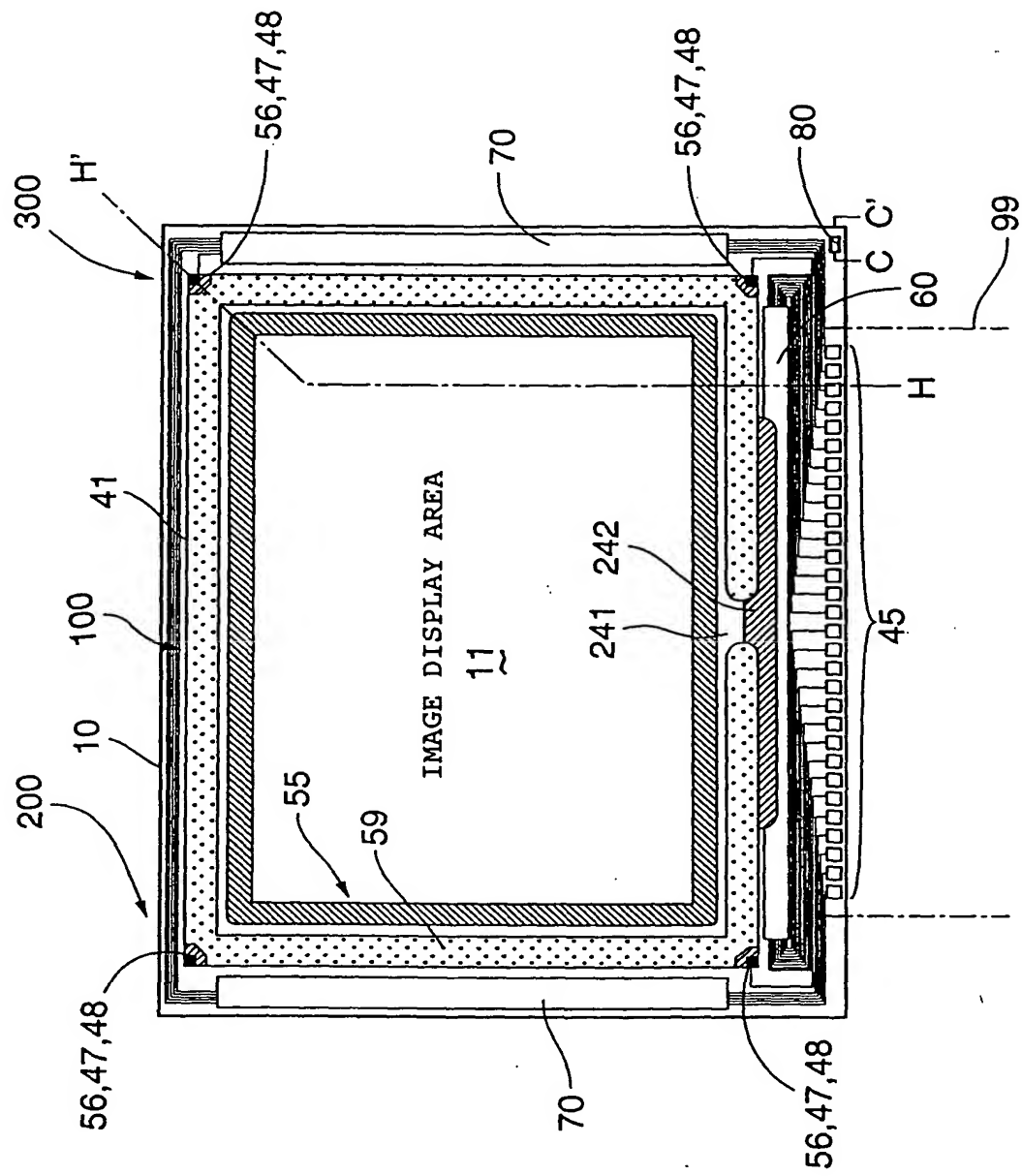


FIG. 2

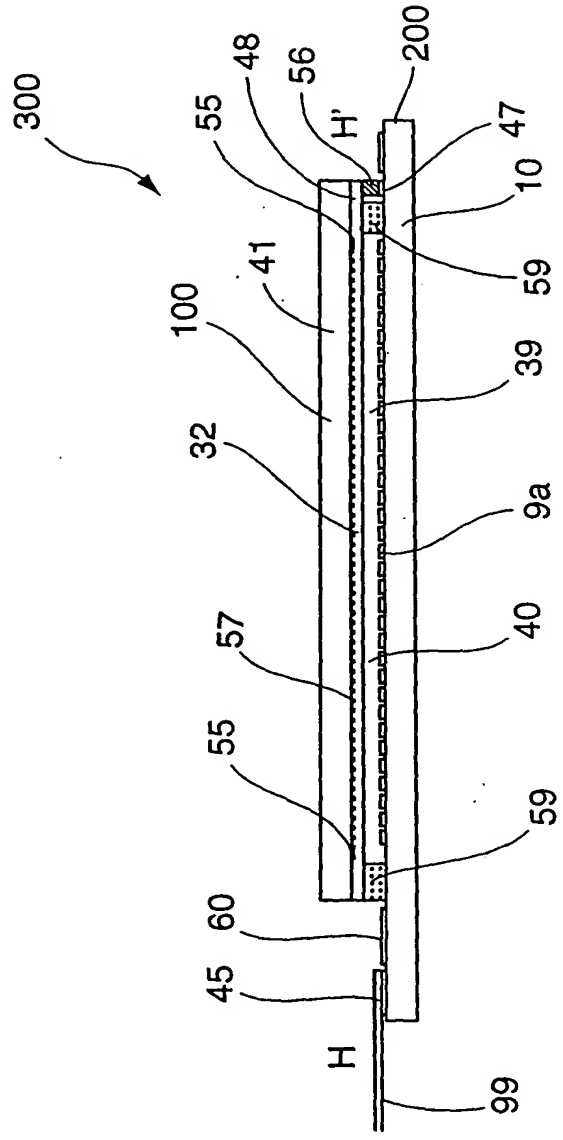


FIG. 3

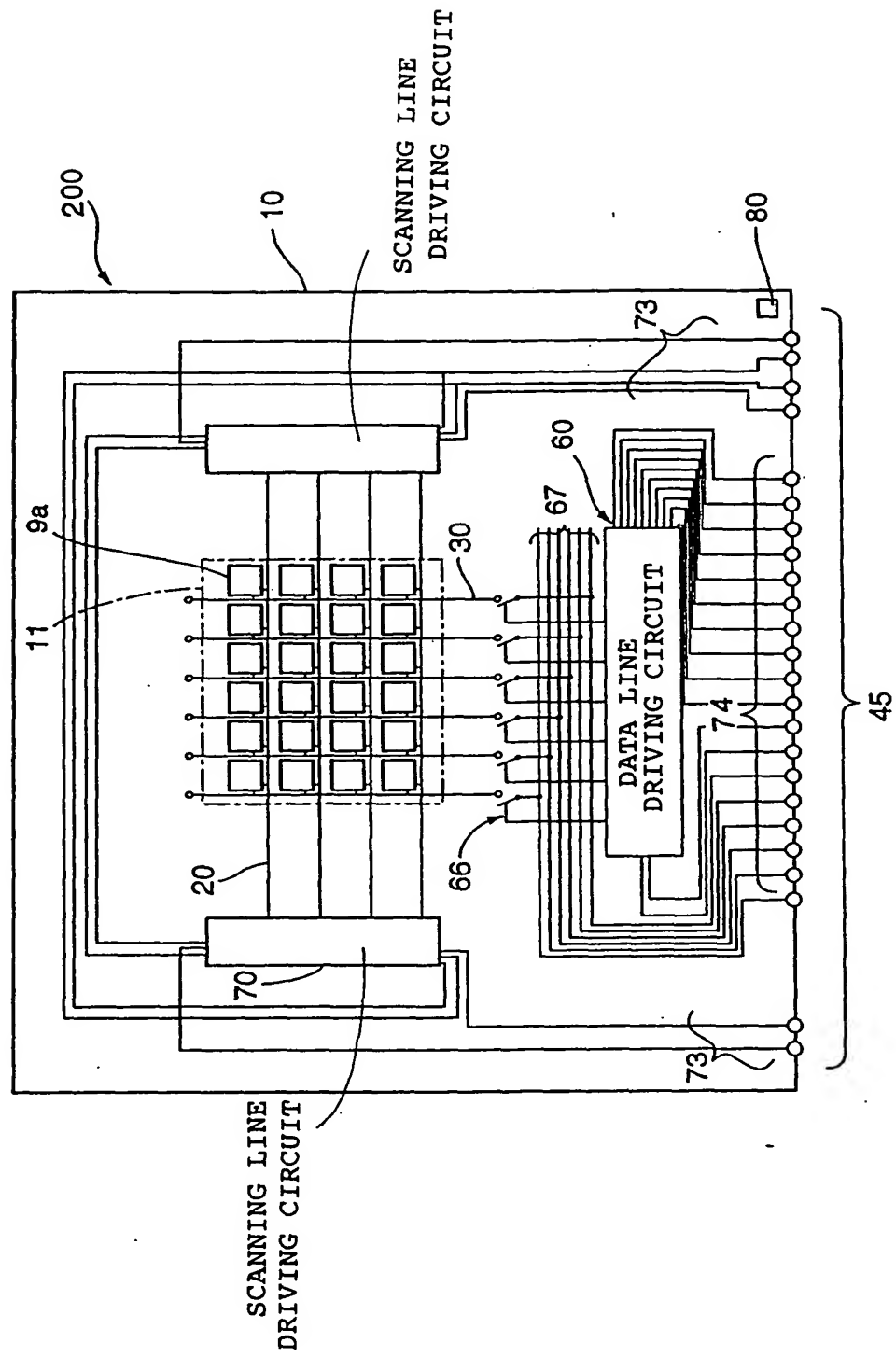


FIG. 4

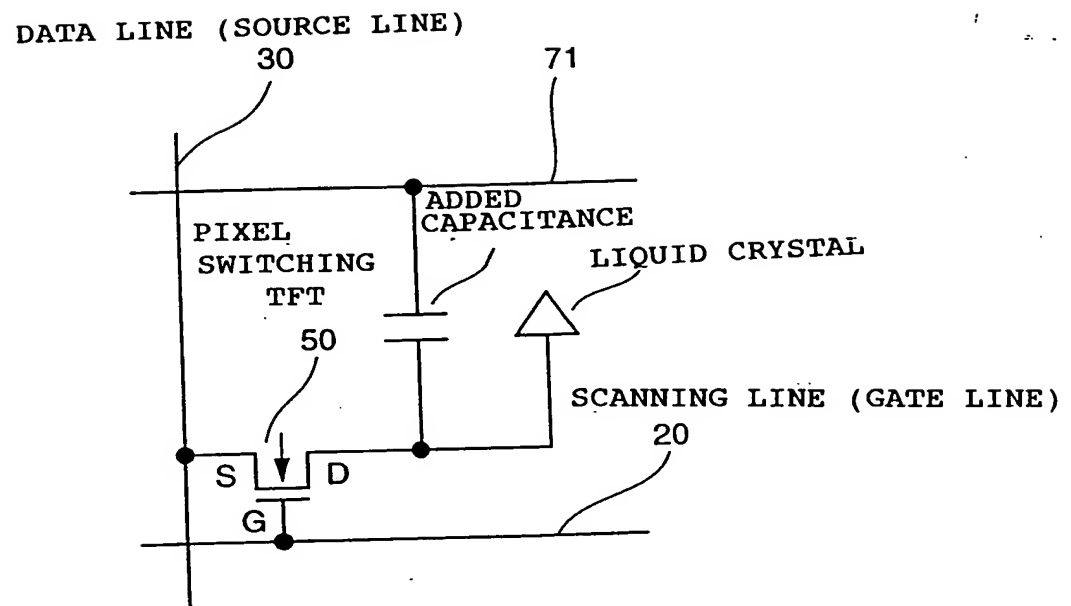
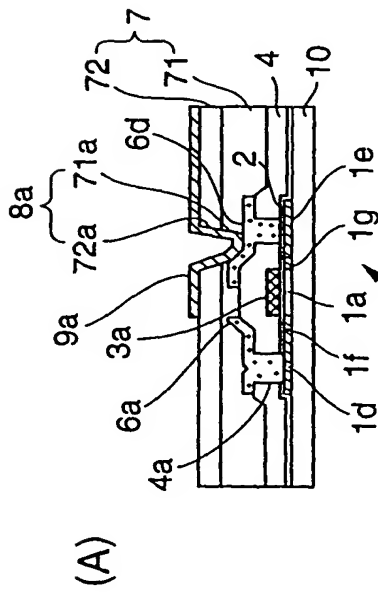


FIG. 5

<PIXEL TET>



<FILM QUALITY EVALUATION REGION>

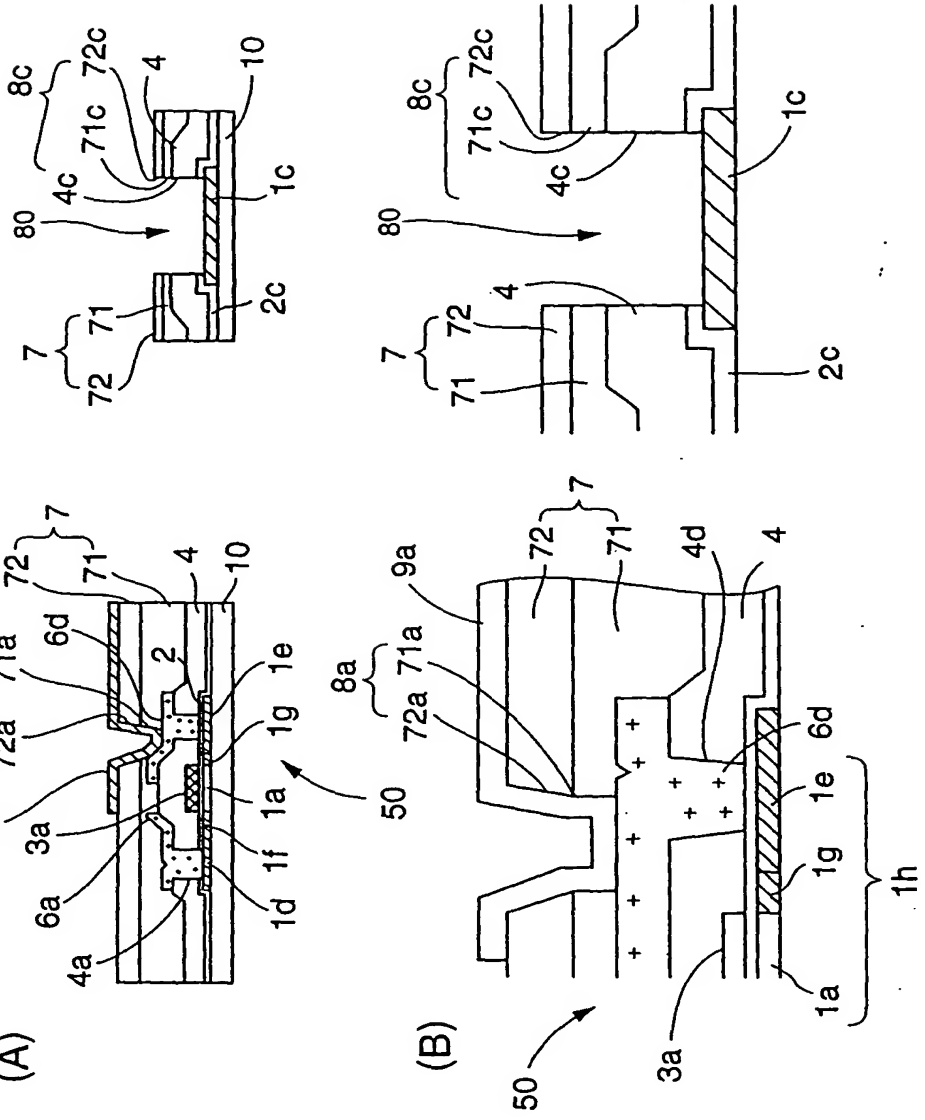


FIG. 6

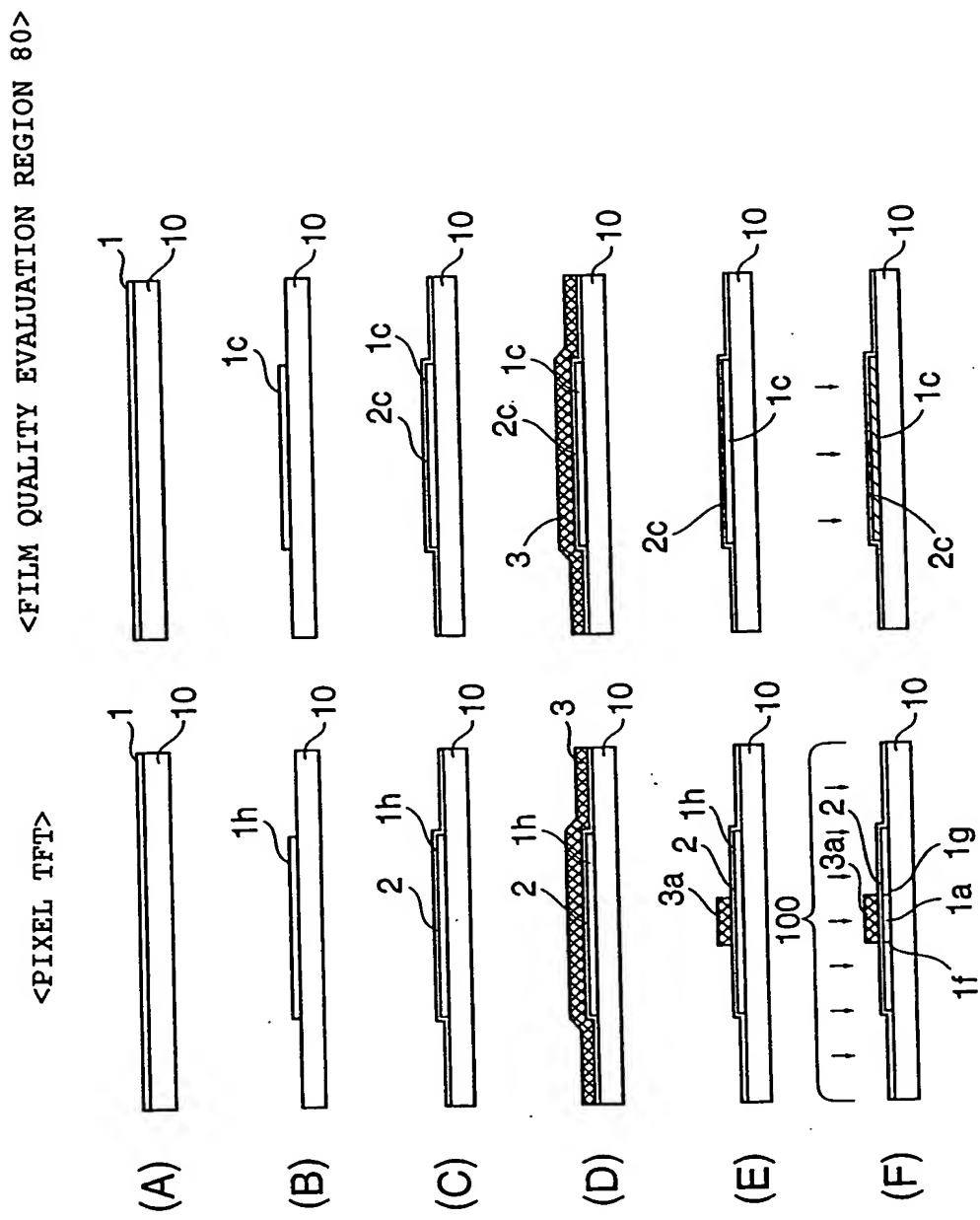


FIG. 7

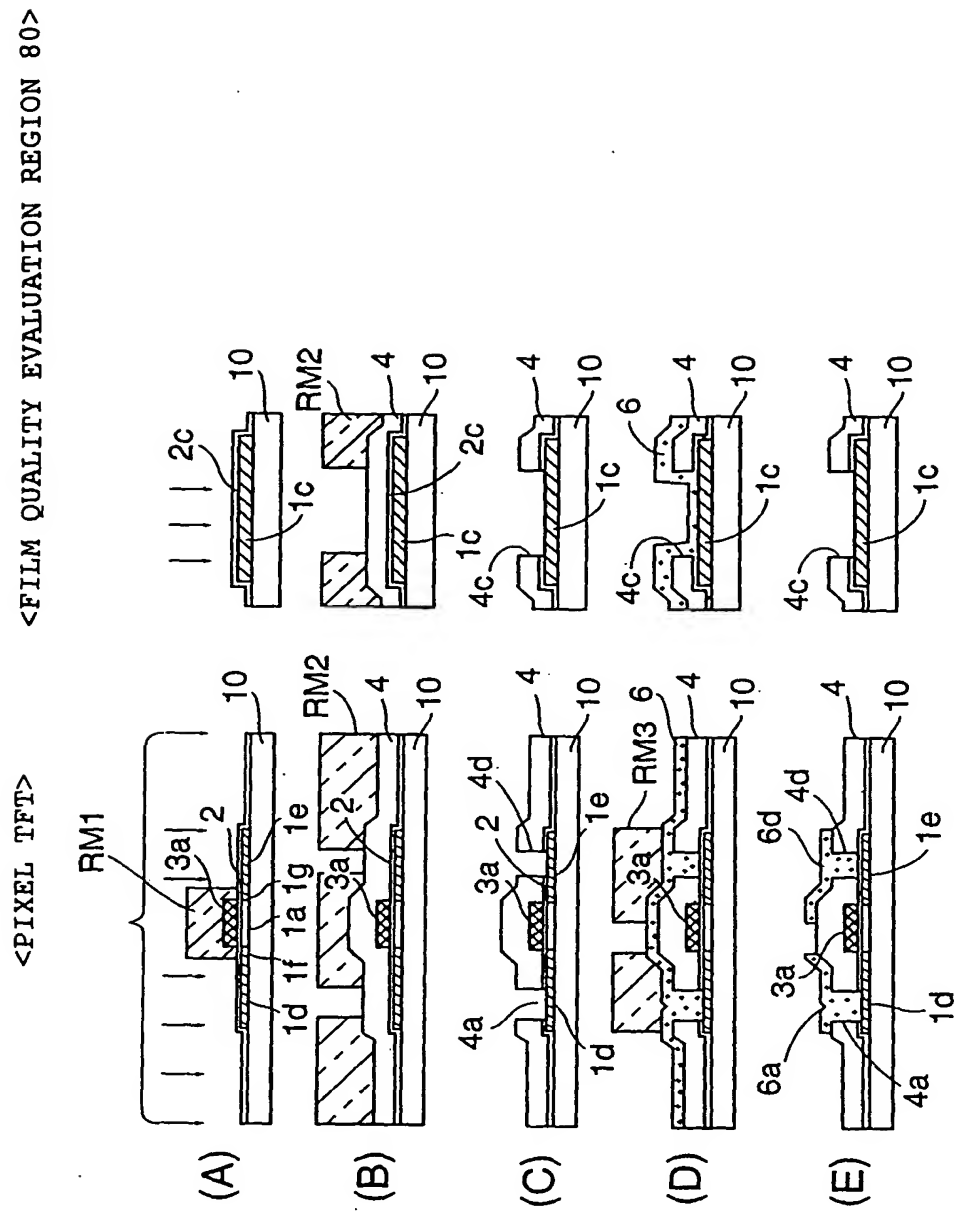


FIG. 8

<PIXEL TFT> <FILM QUALITY EVALUATION REGION 80>

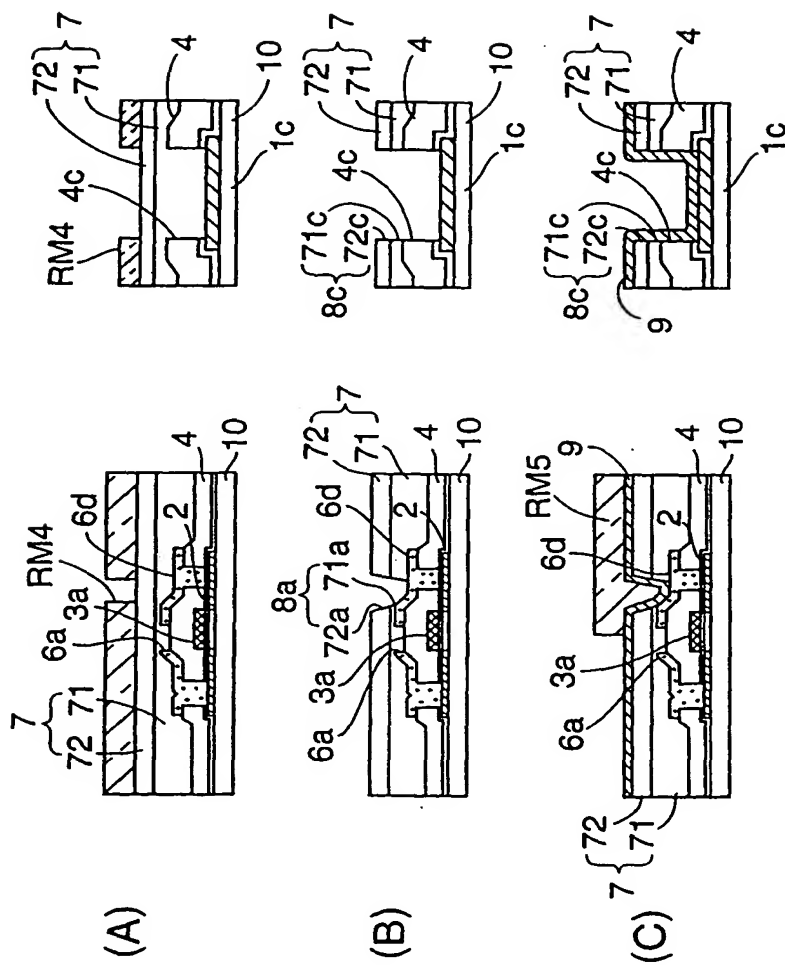


FIG. 9

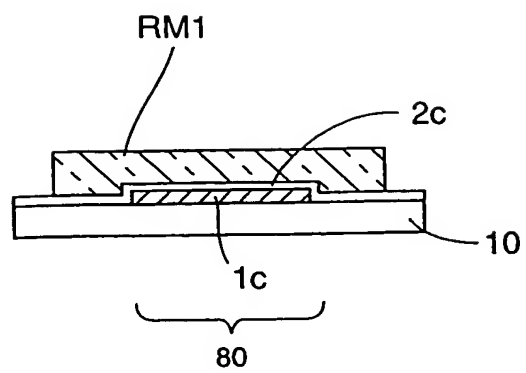
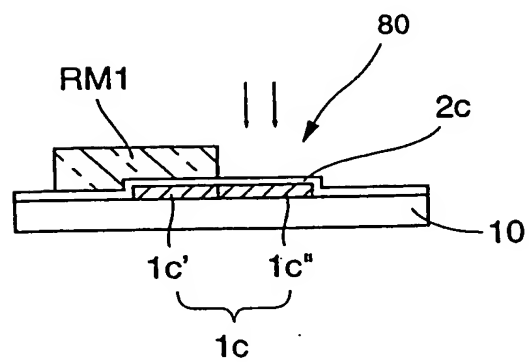


FIG. 10 (A)



(B)

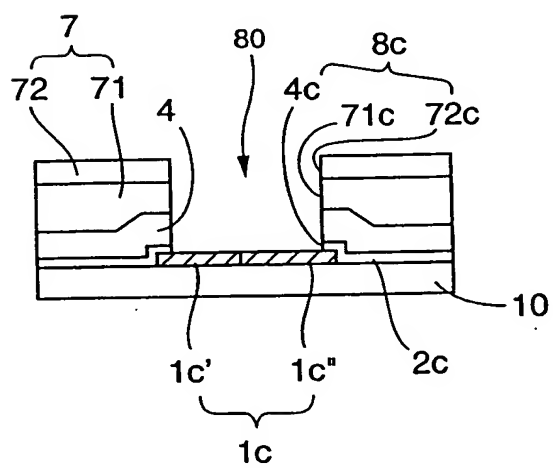


FIG. 11

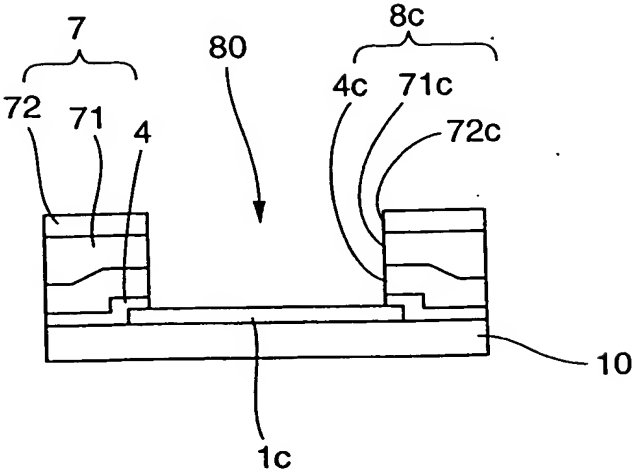


FIG. 12

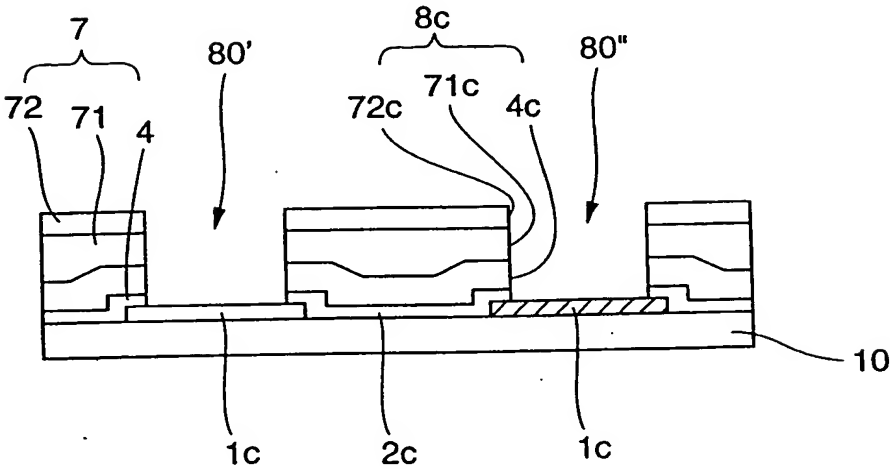


FIG. 13

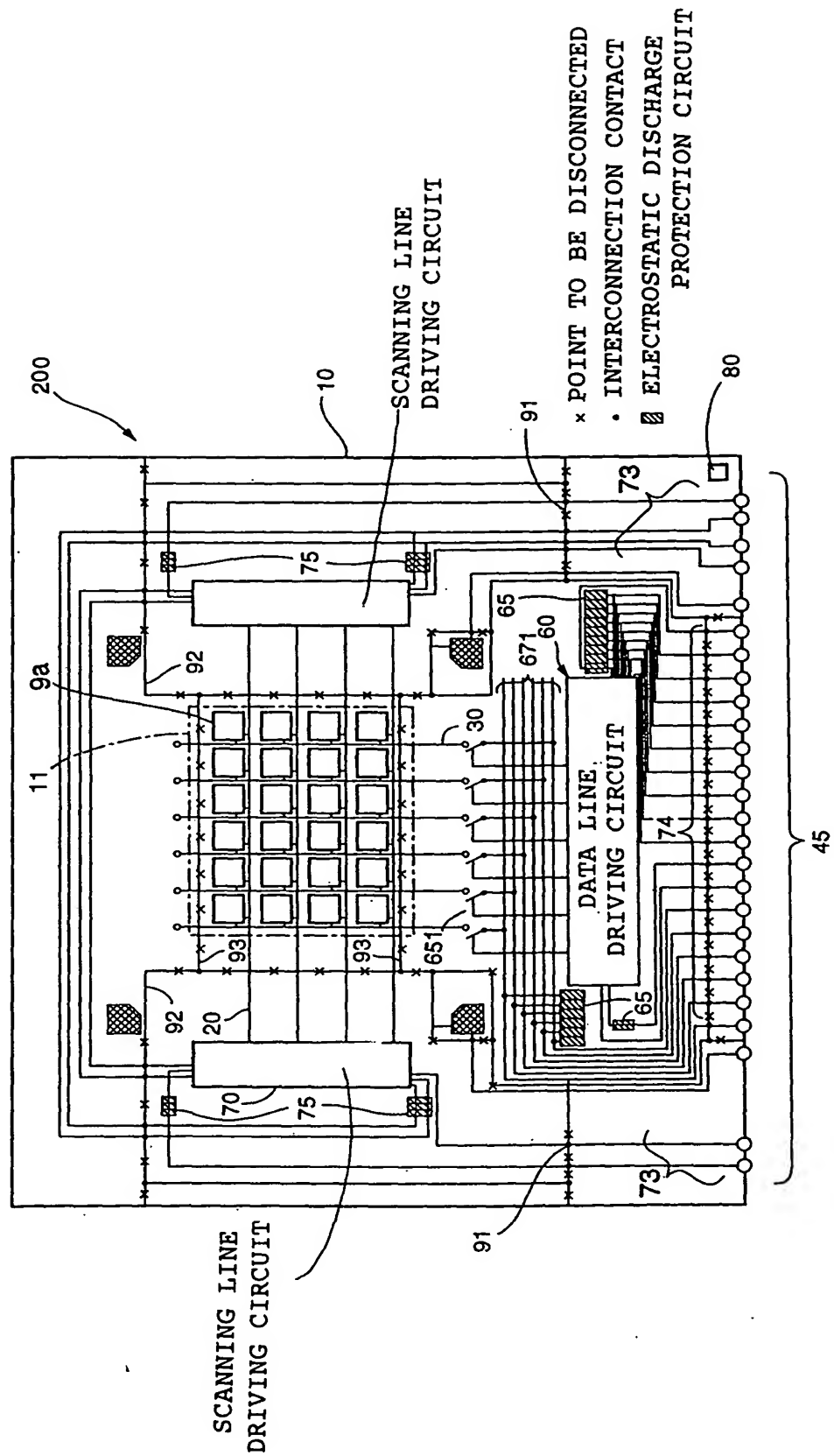


FIG. 15:

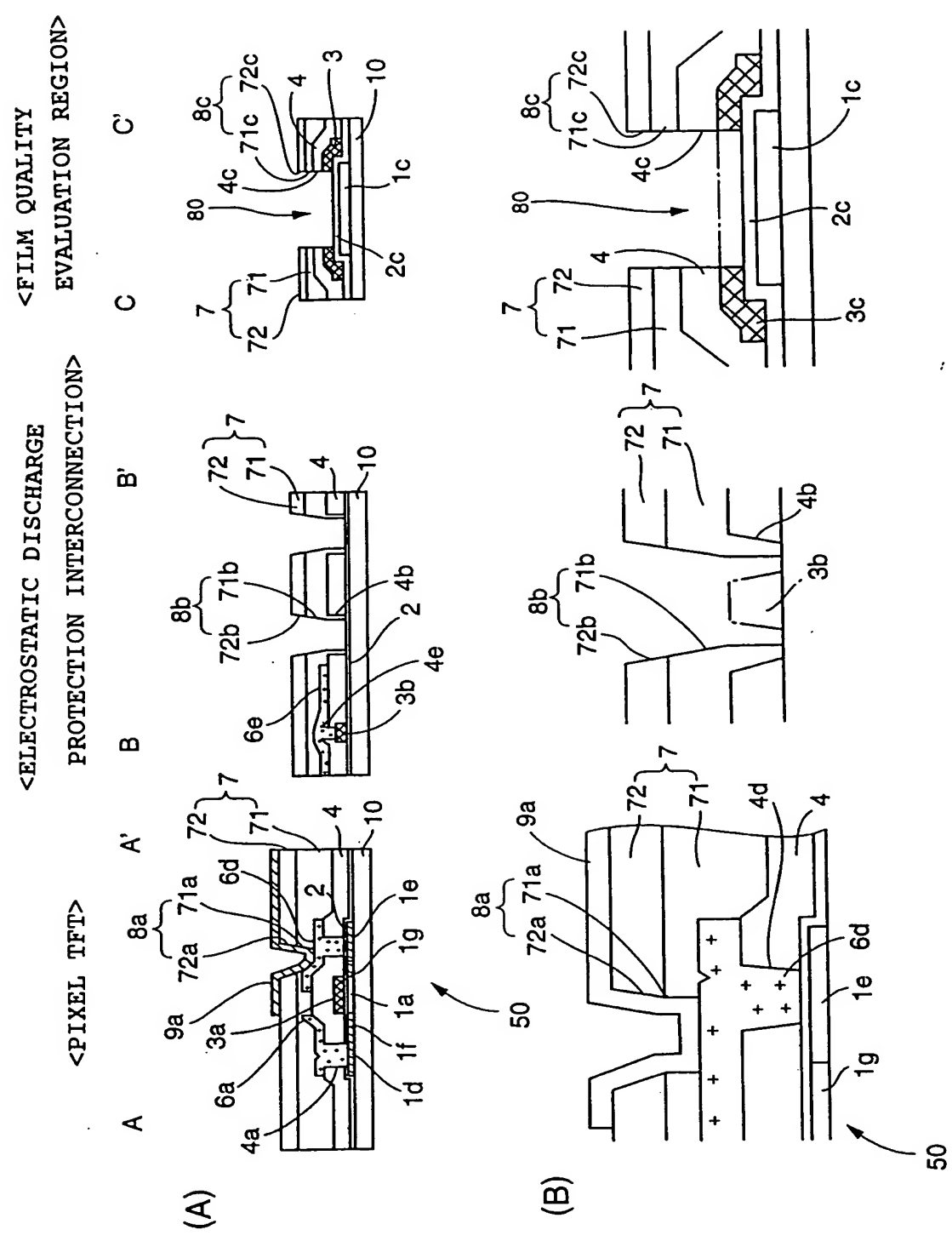


FIG. 16

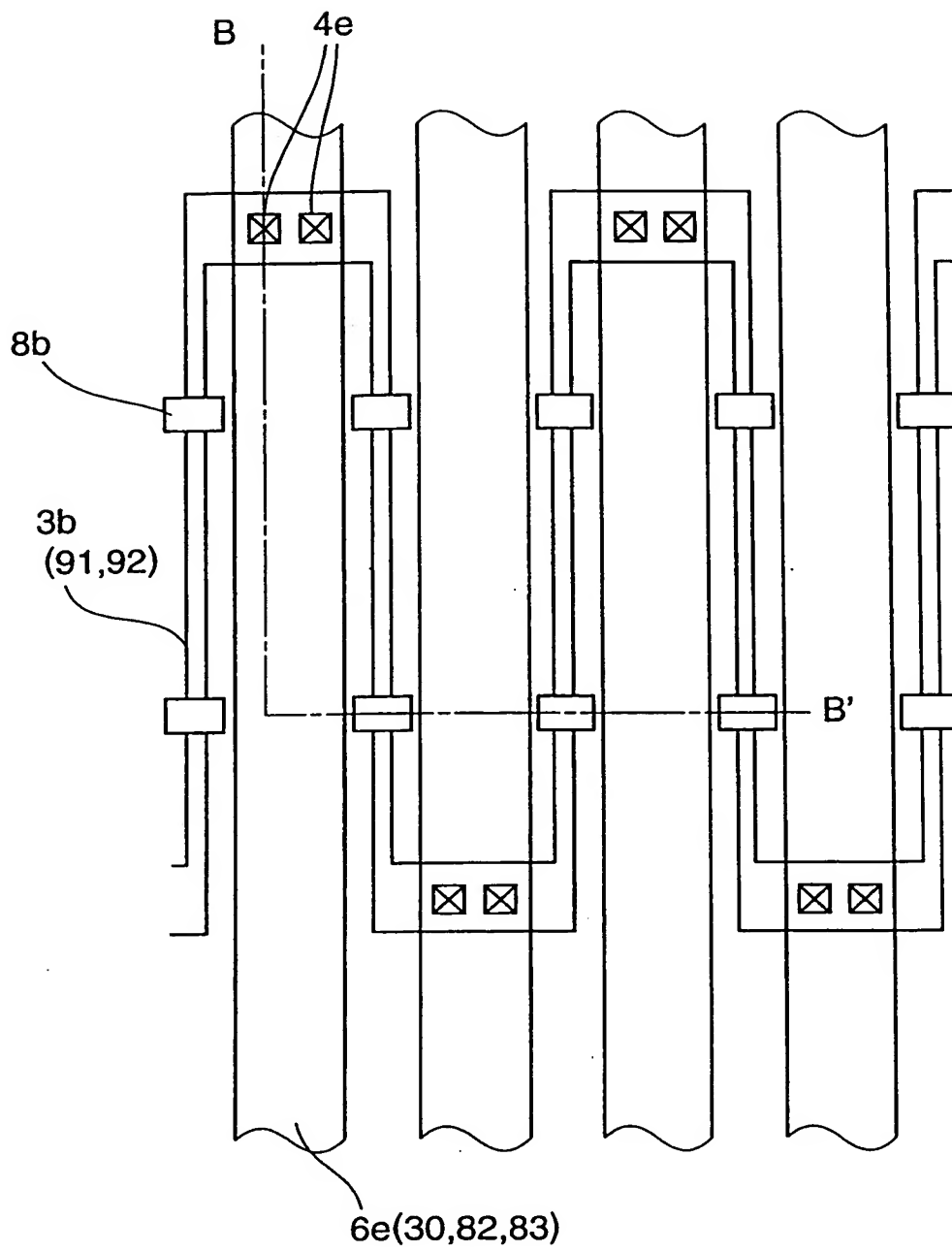


FIG. 17

(ELECTROSTATIC DISCHARGE
PROTECTION CIRCUIT)

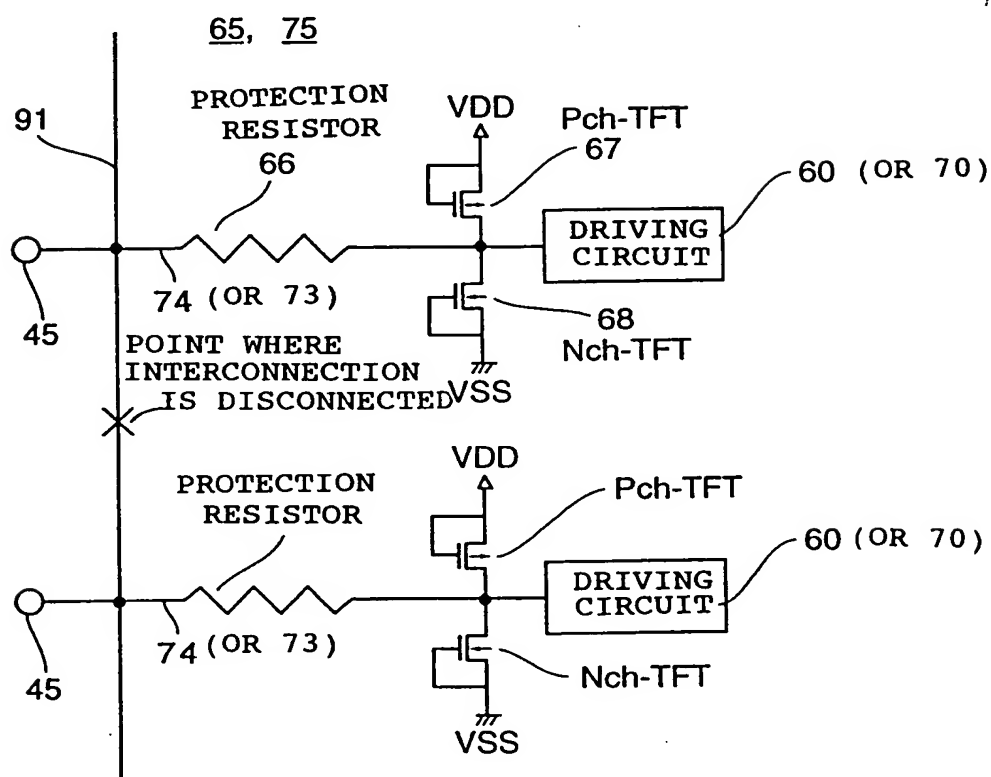


FIG. 18

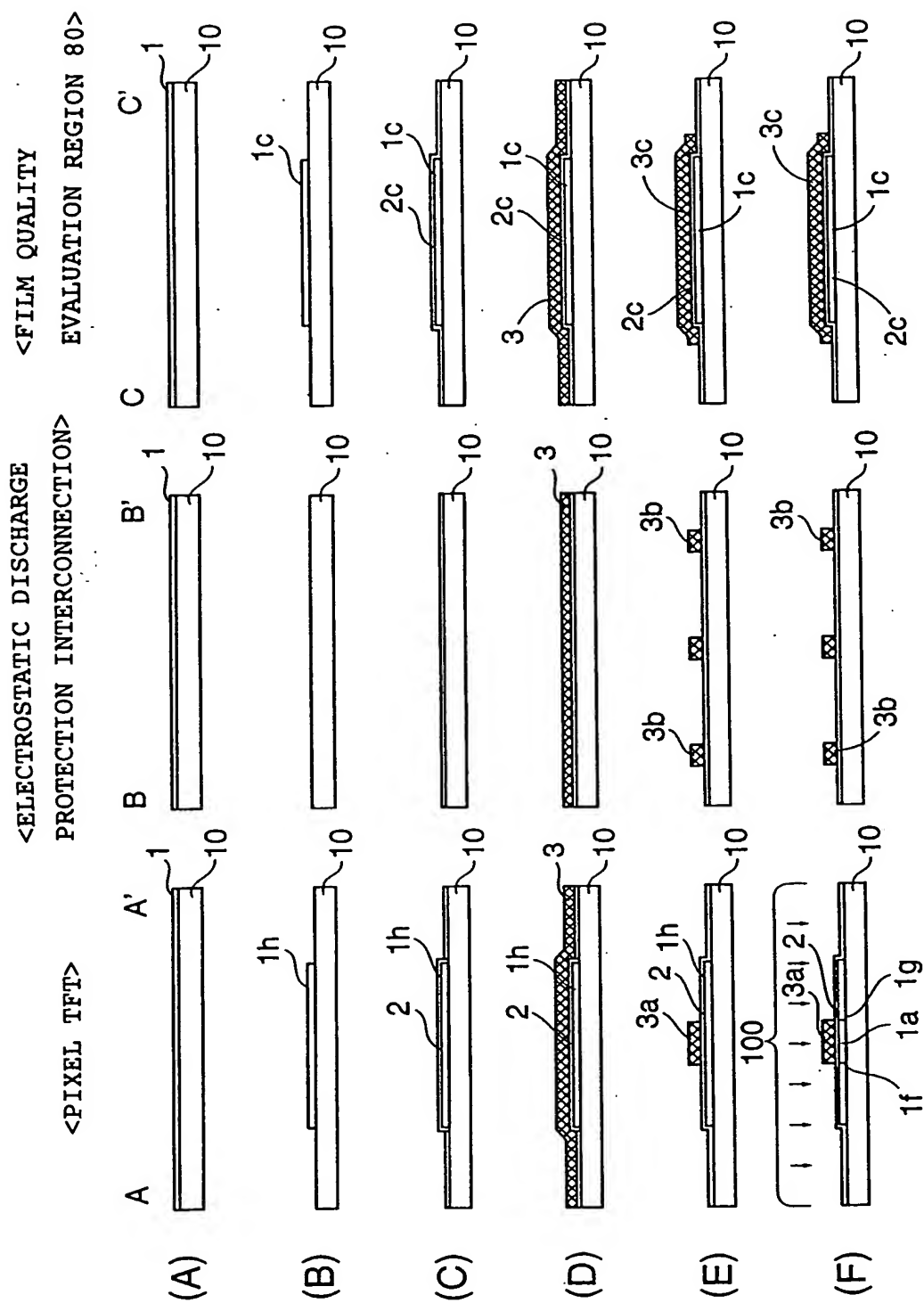


FIG. 19

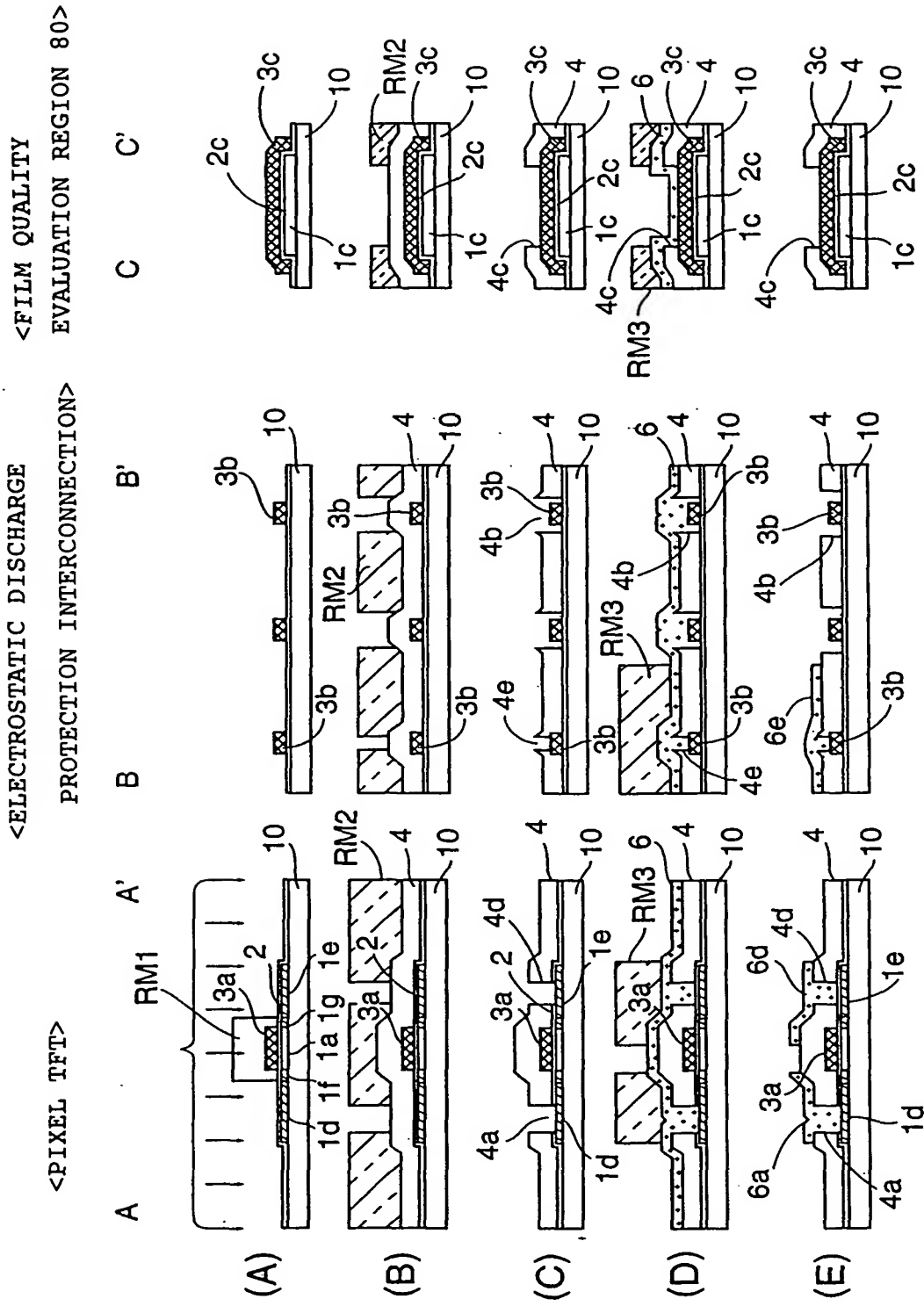


FIG. 20

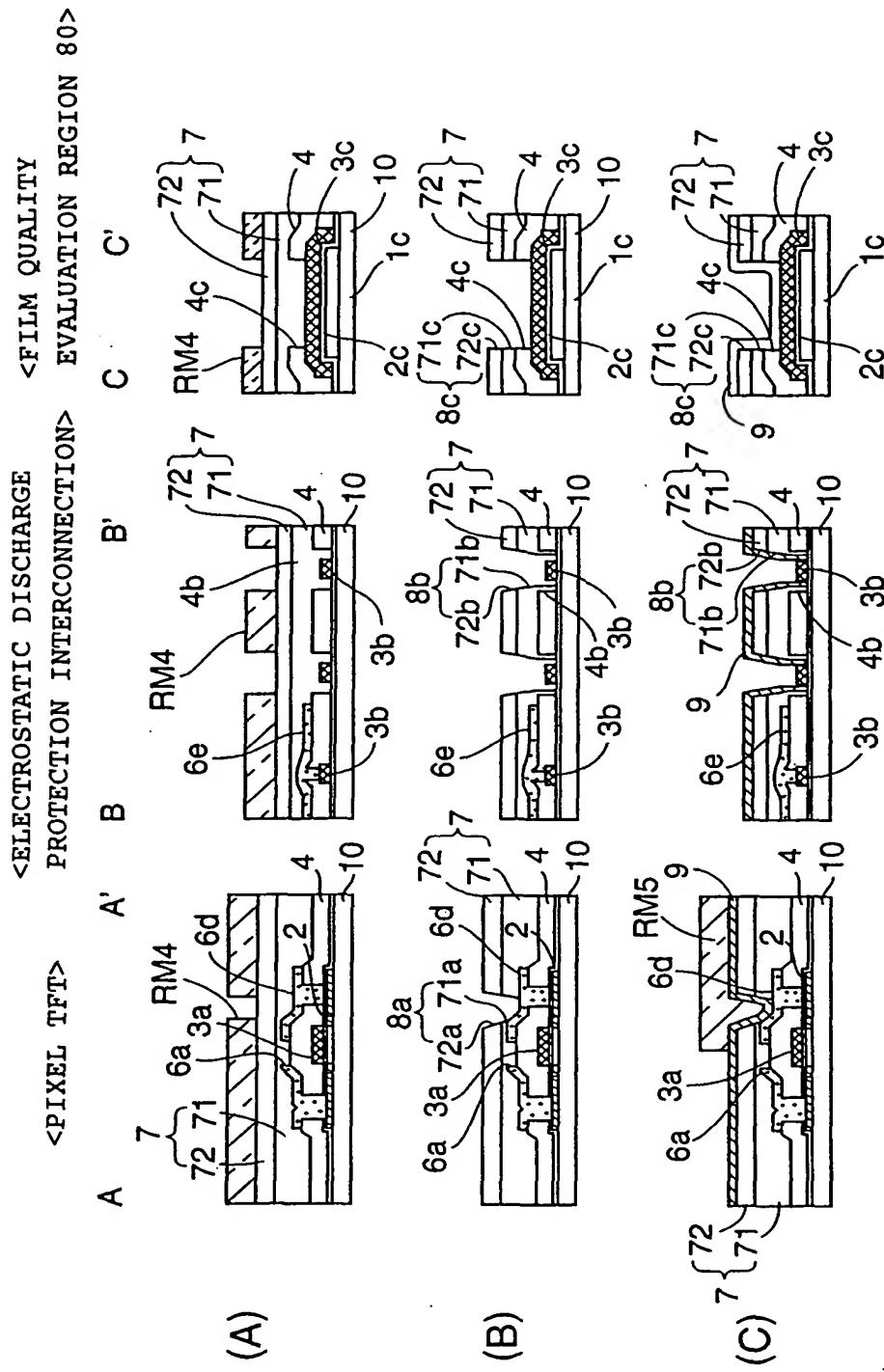


FIG. 21

